



IFW

PATENT

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: NOSOWICZ

Application: **METHOD AND LATCH CIRCUIT FOR IMPLEMENTING ENHANCED
PERFORMANCE WITH REDUCED QUIESCENT POWER DISSIPATION
USING MIXED THRESHOLD CMOS DEVICES**

Serial No.: 10/731,071

Filing Date: December 9, 2003

Art Unit: 2825

Examiner: Tuyen P. To

Case: ROC920030350US1

CERTIFICATE OF MAILING: I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed for Commissioner of Patents, P.O. Box 1450, Alexandria, VA, 22313-1450, on

June 8, 2006 Joan Pennington
Date of Deposit Signature

Joan Pennington

Typed/printed name of person signing

535 North Michigan Avenue
Unit 1804
Chicago, Illinois 60611

Mail Stop Amendment
Honorable Commissioner Of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

In response to the Official Action mailed March 8, 2006, please amend the
above-identified application as follows: